

IEEE SiPS 2026 Bordeaux

IEEE Workshop on Signal Processing Systems

November 1st to 4th, Bordeaux, France



The IEEE International Workshop on Signal Processing Systems (SiPS) is a premier international forum in the area of design, implementation and application of signal processing systems. It addresses all aspects of architecture and design methods of these systems. Emphasis is on current and future challenges in research and development in both academia and industry.

Call for Papers

We invite prospective authors to submit original papers in areas including, but not limited to:

Softw. Implementation of Signal Process. Systems

- Software on programmable digital signal processors
- Application-specific instruction-set processor (ASIP) architectures and systems
- SIMD, VLIW, and multi-core CPU architectures
- GPU-based massively parallel implementation

Machine Learning and AI for Signal Processing

- Circuits and systems for AI
- Resource-efficient machine learning and AI
- Tools/platforms for AI
- Hardware/neuromorphic accelerators
- Hardware/software co-design and automation for AI
- Applications of generative AI and foundation models

Design Methods of Signal Processing Systems

- Optimization of signal processing algorithms
- Compilers and tools for signal processing systems
- Algorithm-to-architecture transformation
- Dataflow-based design methodologies

Hardware Implementation of Signal Process. Syst.

- Low power/complexity signal processing circuits & applications
- FPGA and reconfigurable architecture-based systems
- System-on-chip and network-on-chip
- VLSI for sensor network and RF identification systems
- Quantum signal processing
- Neuromorphic computing
- Edge and embedded computing

Signal Processing Application Systems

- Audio, speech, and language processing
- Biomedical signal processing and bioinformatics
- Image, video, and multimedia signal processing
- Information forensics, security, and cryptography
- Radar, sonar and acoustic sensing systems
- Robotics and autonomous systems
- Sensor arrays and networks and their applications
- Signal processing for non-volatile memory systems
- Wireless communications and internet of things
- Signal processing for mixed-signal technologies

Important Dates

Tutorial+Demo Proposals

May 17th 2026

Special Session Proposals

April 17th 2026

Technical Papers

Submission Deadline: May 3rd 2026

Acceptance Notification: July 10th 2026

Final Submission: September 1st 2026

Early Registration

September 8th 2026

Organizing Committee

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